

**Amendments to the Claims:**

A clean version of the entire set of pending claims (including amendments to the claims, if any) is submitted herewith per 37 CFR 1.121(c)(3). This listing of claims will replace all prior versions, and listings, of claims in the application.

**Listing of Claims:**

1. (Currently Amended) An amplifier device ~~(10)~~, said amplifier device comprising:

amplifier ~~(4)~~ means for amplifying a radio-frequency input signal RFin,  
a controllable feedback loop ~~(4e)~~ for adjusting a gain and a bias voltage applied to the amplifier means ~~(4)~~ for adjusting a current determining a noise level, the feedback loop and the bias voltage being arranged to provide at least two different modes, a first mode providing high gain and low noise for handling weak signals and a second mode providing low-gain power splitting for handling strong signals.

2. (Currently Amended) An amplifier device ~~(10)~~ as claimed in claim 1, wherein the amplifier device ~~(10)~~ comprises mode selection means ~~(15)~~, and the feedback loop ~~(4e)~~ is connected between an output ~~(1a)~~ and an input ~~(1b)~~ of the amplifier means ~~(4)~~, the feedback loop ~~(4e)~~ comprising at least a first resistor ~~(R1)~~ and a second resistor ~~(R2)~~ connected in parallel to each other, and wherein the second resistor ~~(R2)~~ is connected in series with a switch ~~(SW1)~~ controlled by the mode selection means ~~(15)~~, which also control the bias voltage applied to the amplifier means ~~(4)~~.

3. (Currently Amended) An amplifier device ~~(10)~~ as claimed in claim 1, wherein the further comprising mode selection means ~~(15)~~ are arranged to control the feedback loop ~~(4e)~~ and the bias voltage to provide a third mode providing high gain and high current.

4. (Currently Amended) A product comprising the amplifier device (40) as claimed in claim 1 and means for selecting the at least two different modes.

5. (Currently Amended) A method of amplifying a radio-frequency signal, the method comprising the step of:

amplifying the signal in at least a first mode and a second mode by amplifier means having at least two modes, said first mode providing high gain and low noise for weak signals, and said second mode providing low-gain power splitting for handling strong signals,

wherein the amplifier means is turned on in both the first mode and second mode to provide a higher gain and lower noise figure in the first mode, and a lower gain and higher noise figure in the second mode.

6. (Original) A method as claimed in claim 5, further comprising the step of:  
amplifying the signal in a third mode, said third mode providing high gain and high current for medium signals.

7. (New) The amplifier device of claim 1, wherein the amplifier means is an operational amplifier.

8. (New) The amplifier device of claim 1, wherein the high gain and the low-gain are both greater than unity.

9. (New) The amplifier device of claim 1, wherein high gain and the low-gain are both greater than 10 dB.

10. (New) The method of claim 6, wherein the high gain and the low-gain are both greater than unity.

11. (New) The method of claim 6, wherein the high gain and the low-gain are both greater than 10 dB.

12. (New) An amplifier device, comprising:

an amplifier having a first input adapted to receive a radio frequency signal, a second input, and an output adapted to output an amplified RF signal;

a feedback path extending between the output of the amplifier and the input of the amplifier, the feedback path having an impedance that is at least selectable between a first impedance value and a second impedance value;

a bias voltage circuit connected to the second input of the amplifier, the bias voltage circuit being adapted to supply a bias voltage to the second input of the amplifier that is at least selectable between a first bias voltage value and a second bias voltage value; and

a mode selection means adapted to select the impedance of the feedback path and to select the bias voltage to provide at least two different modes, a first mode providing high gain and low noise for handling weak signals, and a second mode providing low-gain power splitting for handling strong signals.

13. (New) The amplifier device of claim 12, wherein the mode selection means is further adapted to provide a third mode providing high gain and high current.

14. (New) The amplifier device of claim 12, wherein the amplifier is an operational amplifier.

15. (New) The amplifier device of claim 12, wherein the feedback path includes at least a first resistor and a second resistor connected in parallel to each other, and wherein the second resistor is connected in series with a switch controlled by the mode selection means.

16. (New) The amplifier device of claim 15, wherein the bias voltage circuit includes:

a third resistor connected between a bias supply voltage and the second input of the amplifier;

a fourth resistor connected to ground; and

a second switch connected between the second input of the amplifier and the second resistor.

17. (New) The amplifier device of claim 12, wherein the bias voltage circuit includes:

a first resistor connected between a bias supply voltage and the second input of the amplifier;

a second resistor connected to ground; and

a switch connected between the second input of the amplifier and the second resistor.